

Partner: Biamp
Model: Tesira
Device Type: Digital Signal Processor



GENERAL INFORMATION

SIMPLWINDOWS NAME:	Biamp Tesira Command Processor RS232 v3.0
CATEGORY:	DSP
VERSION:	3.0
SUMMARY:	This module controls all RS232 communication with the Biamp Tesira.
GENERAL NOTES:	<p>This module is the core RS232 communication module for a suite of modules. The suite of modules utilizes the SIMPL# technology and will only work on the 3-Series Controller. Up to 32 instances of this module can be used in a single program slot. The module has a parameter that allows you to choose one of the 32 instance IDs. Each instance ID can only be used once. The other modules in this suite are control modules. The control modules are responsible for providing the actual control interface in SIMPL. With the SIMPL# technology, the Control modules no longer need to be physically "connected" to the command processor. They register themselves automatically behind the scenes. Each of the control modules also have a command processor ID parameter that you assign to the instance of the command processor to which they report to. You can virtually have an unlimited number of control modules report to a single instance of a command processor.</p> <p>A .tmf file (Demo.tmf) was created for Crestron testing purposes and MUST be used for proper operation of the Tesira Demo program.</p> <p>Once the processing module has determined that it is communicating with the Tesira, it will initialize any individual control modules that are registered to it. Once a control module receives all the responses it is looking for, it will instruct the processing module that its initialization has been completed. The processing module will then request the next control modules initialization. Once all control modules are initialized that are registered with the processing module, the Is_Initialized output on the processing module will go high. At this point, you will have full control of all functionality on the registered control modules.</p> <p><i>Because of the multi-module design, you can cause a lot of traffic on your system by triggering many input signals at the same time. If you have a lot of input signals to trigger at one time, be sure to pace the triggering of the signals allowing the controller to deal with the traffic. Keep in mind the modules, during the initialization process, will get the current state of each of your control points, So you do not need to duplicate this effort. If you have to put the control points into a default states for various room configurations, it is best to use the preset feature built into the Tesira. Trying to automate a preset using SIMPL logic will add a lot of traffic on your system, and will cause adverse effects.</i></p> <p><i>You should wait for any and all processing modules to set "Is_Initialized" to high before attempting to control the Tesira. This is your indication that the Biamp programming is correct and ready to go.</i></p>

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**GENERAL INFORMATION** *continued*

CRESTRON HARDWARE REQUIRED:	3-series processor only (Note: use 1.X modules for 2-series processors)
SETUP OF CRESTRON HARDWARE:	RS232 Baud: 115200 Parity: N Data Bits: 8 Stop Bits: 1
VENDOR FIRMWARE:	Tesira Firmware - 3.5.0.32

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**PARAMETER:**

Command_Processor_ID	Setting to indicate the ID for a particular processing module. Up to 32 separate processing modules may be used in a single program, each one operating independently. Note that if multiple processing modules are to be used in a single program, they must each have different ID's set.
Username	Optional username field for authentication if Tesira Device/System security has been enabled.
Password	Optional password field for authentication if Tesira Device/System security has been enabled.

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**CONTROL:**

Initialize	D	Set this input high to auto-initialize all control modules (once the processing module establishes communication with the Tesira). Pulse to reinitialize all control modules.
Debug	D	Set this input high to allow internal trace messages to be printed in SIMPL Debugger. This is useful for debugging the processes going on inside the compiled SIMPL# code.
From_Device	S	Serial signal to be routed from a 2-way com port.

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**FEEDBACK:**

Is_Initialized	D	Set to high when all registered control modules have successfully indicated that they have received the required responses to all their queries.
Is_Communicating	D	Set to high when the processing module has successfully established communication with the Tesira and is receiving appropriate responses.
To_Device	S	Serial signal to be routed to a 2-way com port.

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**TESTING:**

OPS USED FOR TESTING:	CP3: v1.503.0016
SIMPL WINDOWS USED FOR TESTING:	4.09.04
CRES DB USED FOR TESTING:	66.06.003.00
DEVICE DATABASE:	92.00.002.00
SYMBOL LIBRARY USED FOR TESTING:	1055
SAMPLE PROGRAM:	Biamp Tesira v3.0 RS232 Demo
REVISION HISTORY:	<p>v2.0 – Initial Release</p> <p>v2.1 – Updated to support TesiraLUX. Added 30 more command processor ID to the selection list.</p> <p>v2.2 – No revisions have been performed.</p> <p>v2.3 – No revisions have been performed.</p> <p>v3.0 - Added SIMPL# event callbacks. Reworked module Registration. Fixed issues with RS232 not reestablishing communication after loss of comm. Changed around how the subscription watchdog logic works. Once initialization is achieved, the subscription watchdog begins a patrolling or revolving method that tests each subscription capable registered instance tag.</p>